Subject Description Form

Subject Code	EE4007 / EE4007A / EE4007B
Subject Title	Advanced Power Electronics
Credit Value	3
Level	4
Pre-requisite/ Co-requisite/ Exclusion	Pre-requisite for EE4007: EE3003 Pre-requisite for EE4007A: EE3003A Pre-requisite for EE4007B: EE3003B Exclusion: EE521
Objectives	 To provide the students with the knowledge of advanced power electronic conversion. To ensure the students having an in-depth understanding of the design and control of various power electronics converters. To give the knowledge of AC switched-mode conversion. To provide a concept of impact of power electronics on power quality.
Subject Intended Learning Outcomes	 Upon completion of the subject, students will: a. Have acquired a good understanding of basic switched-mode DC/DC topologies, operation, performance and modelling. b. Have acquired a basic understanding of resonant converters and its method of loss reduction. c. Be able to apply switched-mode techniques to inverters (DC/AC converters). d. Be able to perform study on power electronics circuit simulation. e. Be aware of impacts of electromagnetic interference (EMI) and reduction of EMI using power electronics techniques. f. Be able to present results of study in the form of computer simulation, design equations and basic models, working independently and in teams when conducting power electronics circuit design.
Subject Synopsis/ Indicative Syllabus	 Pulse-width-modulated DC/DC Converters: Basic topologies and higher order converters, transformer-isolated topologies, snubber circuits, continuous and discontinuous conduction modes of operation, ripple analysis. Resonant-mode DC/DC Converters: Classification, zero-current switching and zero-voltage switching techniques, quasi-resonant converters, resonant transition converters. Switched-mode Inverters: Single-phase and three-phase voltage-source inverters, AC/AC conversion, resonant inverters. Modelling and Control of Power Converters: Small-signal modelling, traditional PID control method, modern control techniques, analogue and digital circuit simulation for power electronics, simulation techniques. Electromagnetic Interference: Generation of EMI, power factor, switched-mode EMI filter, International Standards, reduction of EMI. Laboratory Experiments Conduct hardware experiments on DC-DC converter.

Teaching/Learning Methodology	 Lectures and tutorials are effective teaching methods: To provide an overview or outline of recent development of power electronics. To introduce new concepts and knowledge in advantage power electronic converter design, soft switching techniques, control methods and electromagnetic interference (EMI) aspects. To explain difficult ideas and concepts. To provide students feedback in relation to their learning. To encourage students' responsibility for their learning by extra reference books reading and computer-based circuit simulations. Laboratory works is an essential ingredient of this subject: To provide power converter design experience for the students. To provide deep understanding of various power converter design aspects. To enable students to organise principles and challenge ideas. 								
	Teaching/Learning methodology	thodology Outc				mes			
	Lectures	a ✓	D ✓	C ✓	d		e √	1	
	Tutorials	✓	✓	✓			✓		
	Experiments	\checkmark	\checkmark	✓	√		√	\checkmark	
Assessment Methods in Alignment with	Specific assessment methods/tasks	% weight	% Intended sul thing outcomes to a b			be assessed c d e f			
Intended Learning	1. Examination	60%			✓		✓		
Outcomes	2. Tests 3. Laboratory reports	20%			 ✓	✓	 ✓		
	4 Assignments	10%			· ·	•	· ·		
	Total	100%	6						
	The understanding on theoretical principle and practical considerations, analytical skills and problem solving techniques will be evaluated. Examination, class tests, laboratory sections and reports are an integrated approach to validly assess students' performance with respect to the intended subject learning outcomes.								
Student Study	Class contact:								
Effort Expected	Lecture/Tutorial					33 Hrs.			
	Laboratory					6 Hrs.			
	Other student study effort:								
	 Laboratory preparation/report/assignment 					12 Hrs.			
	Self-study 54 H						54 Hrs.		
	Total student study effort105 I							05 Hrs.	
Reading List and References	 Textbooks: Ned. Mohan, Power Electronics: K.W.E.Cheng, Classical Switched Kong Polytechnic University, 200 G. M. Masters, Renewable and eff 2004. Reference books: N. Mohan, Power Electronics: A A.M. Trzynadlowski, Introduction John Wiley & Sons, 2015. 	Converte 1 Mode a 02 ficient el First Cou	ers, Apj ind Rese ectric p urse, Jo odern	plicatio onant P ower sy hn Wil Power	ns & D ower C /stems, ey & So Electro	esign, onver John ons, 20 nics, 7	Wile ters, T Wiley 012. Third	y, 2007 The Hong 7 & Sons, Edition,	

3, Muhammad H. Rashid, Power Electronics: Devices, Circuits and Applications 4th ed, Pearson India, 2017.
3. Robert W. Erickson, Dragan Maksimović, Fundamentals of Power Electronics, Springer; 3rd ed. 2020
 Farzin Asadi, Simulation of Power Electronics Circuits with MATLAB®/Simulink®: Design, Analyze, and Prototype Power Electronics, Apress, 1st ed, 2022.

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