## Subject Description Form

Subject Code	EE2002C
Subject Title	Circuit Analysis
Credit Value	3
Level	2
Pre-requisite/ Co-requisite/ Exclusion	Nil
Objectives	<ol> <li>Introduce fundamental circuit theory.</li> <li>Develop ability for solving problems involving electric circuits.</li> <li>Develop skills for experimentation on electric circuits.</li> </ol>
Intended Learning Outcomes	<ul><li>Upon completion of the subject, students will be able to:</li><li>a. Acquire a good understanding of fundamental circuit theory.</li><li>b. Solve simple problems in electric circuits.</li><li>c. Use suitable instrumentation to carry out experimental investigations to validate the theoretical investigations.</li></ul>
Subject Synopsis/ Indicative Syllabus	<ol> <li>Syllabus:         <ol> <li><u>DC Circuits</u></li></ol></li></ol>

	5. Electrical Measurement						
	Measurement uncertainties. Resistance measurement: Four-probe measurement and Wheatstone Bridge. Capacitance and inductance measurement using AC Bridges.						
	Power Measurement. Measuring three-phase power by two-wattmeter method.						
	Laboratory Experiments:						
	1. Basic Instrumentation						
	2. Kirchhoff's laws and the maximum power transfer theorem						
	3. RC and RL circuits						
Teaching/ Learning Methodology	Lectures, supplemented with interactive questions and answers, and short quizzes	a, b	knowledge comprehensi	<i>nsion</i> is strengthened with e Q&A and short quizzes. s, students <i>apply</i> what they have solving the problems given by <i>acquire</i> hands-on experience in ctronic equipment and <i>apply</i> have learnt in lectures/tutorials nentally validate the theoretical			
	Tutorials, where problems are discussed and are given to students for them to solve	a, b					
	Laboratory sessions, where students will perform experimental verifications. They will have to record results and write reports on the experiments.	b, c	using electr what they ha				
	Assignment	a, b	will develop				
Assessment Methods in Alignment with Intended Learning	n Specific assessment methods/task t with		% Weighting	Intended Subject Learning Outcomes to be Assessed			
Outcomes				а	b	c	
	1. Continuous Assessment (Tota	ıl 40%)					
	<ul> <li>Assignment</li> </ul>		16%	✓	✓		
	<ul> <li>Laboratory works and reports</li> <li>Mid-semester test/Short quizzes</li> <li>2. Examination</li> <li>Total</li> </ul>		18%	✓	✓	✓	
			16%	~	~		
			50%	~	$\checkmark$		
			100%				
	L		I	1			

Specific asso methods/tas	essment				
		Remark			
Assignment		competence level of <i>knowledge</i> criteria (i.e. <i>what</i> to be demons <i>extent</i> ) of achievement will be	are given to students to assess their evel of <i>knowledge</i> and <i>comprehension</i> . The <i>what</i> to be demonstrated) and level (i.e. the nievement will be graded. Feedback about ance will be given promptly to students to provement their learning.		
Laboratory reports	works and	Students will be required to perform three experiments and submit reports on the experiments. This is to enhance and evaluate the students' problem solving techniques, ability to apply what they have learnt, and organization skills.			
Mid-semeste Quizzes	er test/ Short	There will be a mid-semester test/short quizzes to evaluate students' achievement of all the learning outcomes and give feedback to them for prompt improvement.			
Examination	1	There will be an examination to assess students' achievement of all the learning outcomes. These are mainly summative in nature.			
Student Study Class contact	:				
Effort Expected • Lecture		22 Hrs.			
Tutorial		8 Hrs.			
Laborator	v	9 Hrs.			
Other student	-				
	and Assignme	43 Hrs.			
Report W		18 Hrs.			
		100 Hrs.			
<b>References</b> 1. C.K. Alex	<ul> <li>Textbook:</li> <li>1. C.K. Alexander and M.N.O. Sadiku, Fundamentals of Electric Circuits, 6<sup>th</sup> Edition, New York: McGraw-Hill, 2017.</li> </ul>				
References:	References:				
	1. G. Rizzoni and James Kearns, Principles and Applications of Electrical Engineering, 6 <sup>th</sup> Edition, New York: McGraw-Hill,2016.				
2. W.H. Hay					
3. A.H. Rot					