Jobshop Scheduling with Variable Duration and Multiple Resources

Yihua LI, ILOG in UK

Jobshop scheduling is to find a set of task starting times on each machine to minimize total make span. There are two types of constraints involved in the problem: one is task sequence requirement within each job; the other is a limit of machine resources (each machine can only processes one task each time). The second type of constraint is non-linear, which leads the problem difficult to be solved optimally within a reasonable computing time.

I find a way to convert those non-linear constraints into linear ones by introducing a set of boolean variable and convert the problem to a linear mixed integerprogramming problem. I also design a specific solution method to solve the problem optimally and quickly. For a problem with 12 jobs, each job having 12 tasks on 12 machines, I solved it in 15 seconds comparing with 38 minutes by Scheduler/Solver and 45 minute by CPLEX.

Now we are dealing with a problem with variable durations on multiple machines, and try to solve it optimally and efficiently. A mathematic model is formulated as follows:

Minimize
$$z$$
 (makespan) (1.0)

subject to:

$$z \ge x_{in} + d_{in}, \quad \forall i \in I$$
 (Minimize max makespan) (1.1)

$$x_{i,j+1} \ge x_{ij} + d_{ij}, \quad \forall_i \in I, \forall_j \in J_i$$
 (Task sequence) (1.2)

$$y_{ij} = x_{is}, \quad \forall_i \in I, \forall_j \in S_i$$
 (Supper task start) (1.3)

$$y_{ij} + t_{ie} = x_{ie} + d_{ie}, \quad \forall_i \in I, \forall_j \in S_i \text{ (Supper task end)}$$
(1.4)

$$y_{ij} - y_{i'j'} - t_{i'j'} \ge M(b_{ij} - 1), \quad \forall_{ij} \in R \text{ (resource limit)}$$
(1.5)

$$y_{i'j'} - y_{ij} - t_{ij} \ge -Mb_{ij}, \quad \forall_{ij} \in R \text{ (resource limit)}$$
(1.6)

$$y_{ij} - y_{i'j'} - t_{i'j'} \ge M(B_{ij} - 1), \quad \forall_{ij} \in A \text{ (resource limit)}$$
(1.7)

$$y_{i'j'} - y_{ij} - t_{ij} \ge -MB_{ij}, \quad \forall_{ij} \in A \quad \text{(resource limit)} \quad (1.8)$$

$$\sum_{i} \sum_{j} B_{ij} \le 1$$
 (Arm resource) (1.9)

all
$$x_{ij}, y_{ij}, \ge 0$$
, int eger; $b_{ij}, B_{ij} = 0, 1$ (2.0)

where:

- A -a set of tasks using Arm resource,
- d_{ij} duration of basic task *j* in job *i*,
- I a set of job,
- J_i a set of basic tasks in job *i*,
- \mathbf{R} a set of resources,
- S_i a set of supper tasks in job *i*,
- M big constant,
- t_{ij} duration of supper task *j* in job *i*,
- b_{ii} 1—task *i* is early than task *j*, 0--reversed,
- B_{ii} 1—task *i* and task *j* are overlapped on Arm resource , 0--otherwise,
- x_{ii} starting time of basic task *j* in job *i*,
- y_{ij} starting time of supper task *j* in job *i*,
- z makespan, maximum completion time for all jobs,

This is an Integer Linear Programming problem, which is NP-Completed hard. According to the special structure of the problem, we develop a heuristic to get an initial feasible solution quickly, and then call CPLEX to solve the problem optimally. Since this model is so general, it can deal with most of Jobshop scheduling problem in real world. That is, either there are constant or variable durations, either each task is processed by one or more machine(s) simultaneously, either one machine can process one or more task(s) simultaneously; an optimal solution can be obtained by a heuristic and CPLEX within a reasonable computing time. For a testing, a Wafer Chip Processing problem with 4 jobs and each job having 20 tasks over 9 machines (See Figure 1) has been solved optimally in less 30 seconds.

Marathon Express 800

Marathon Express 800 platforms are a high productivity, high throughput 8-sided process integration system with a dual load lock ideally suited for PVD and CVD applications, as well as Etch. Utilizes VCE 6 vacuum cassette elevators and are available in four levels of integration. Will handle substrate sizes up to 200mm round and 6"x6" square and will fully integrate to Factory Automation solutions such as AGV and SMIF.





MagnaTran 7 is available in 2 axis (R, θ) and 3 axis (R, θ , Z) models, with 3 different patented arm configurations in various sizes, and with a variety of passive support end effectors, for handling up to 300mm size wafers or similar substrates.

Figure 1. Brooks Automation



	4 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	1	2
xP 1			2
PMS			
RLPM	Water4 ScaraPickAndAlign	Waters BearaPitkAndAlign	
R Aligr	Water4 ScaraPickAndAlign	Waters ScaraPickAndAlign	
RMS			
OCK1		(fer 3 ArmP)	tk in5 StaraPlai
RLL1.		ifer 3 Arm Pl	itk in5 StaraPlay
RLL1			
RLL1		(fer 3 ArmP)	ICH Int BoaraPlay
OCK2	n4 Sra	aPia	Wafer & Pump
RU2	er4 Star	aPia	Wafer4 Pump
RLL2			
RLL1	or4 See	aPla	
1.15			
RPMI 0	sfer1 PickPM Visi PlaceP	Water2 Process >	Visit 1 Rec 1 - Edep 30
RPM2		rt PlacePMV	Watert Process Visit 2 Rec 1 - Stlen 30
RPM3			
E PM4			
RPMS			
RPM6			
	R LL1 R LL1 R LL1 R LL2 R LL2 R LL2 R LL2 R LL2 R PN1 R PM1 R PM1 R PM2 R PM3 R PM4 R PM5	R LL1 R LL1 R LL1 R LL2 R L 2 R R R R R R R R R R R R R R R R R R R	RLL1 Ife:3.4mPi RLL1 Ife:3.4mPi RLL1 Ife:3.4mPi RLL2 Ife:3.4mPi RL2 Ife:3.4mPi RPM3 Ife:3.4mPi RPM4 Ife:3.4mPi RPM4 Ife:3.4mPi RPM4 Ife:3.4mPi

Table 1. Gantt Chart for processing task scheduling by **JView** of ILOG.